IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Granneman et al.

App. No. : Unknown Filed : Herewith

For : METHOD AND APPARATUS FOR

SUPPORTING A SEMICONDUCTOR

WAFER DURING PROCESSING

Examiner : Unknown

Group Art Unit : Unknown

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing 51 references, 13 of which are enclosed, and 38 of which are not enclosed pursuant to 37 CFR § 1.98(d) as they were either cited by or provided to the Patent Office in parent application No. 09/717,702, filed November 20, 2000, from which priority is claimed. This Information Disclosure Statement is being filed within three months of the filing date of this application or upon filing if this is a CPA or RCE, and no fee is required in accordance with 37 C.F.R. § 1.97(b)(1), (b)(2), or (b)(4).

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: June 24,2003

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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY, DOCKET NO. ASMINT 002C3	APPLICATION NO. Unknown
INFORMATION DISCLOSURE STATEMENT		
BY APPLICANT	APPLICANT Granneman et al.	
(USE SEVERAL SHEETS IF NECESSARY)	FILING DATE Herewith	GROUP Unknown

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(USE SEVERAL SHEETS IF NECESSARY)	FILING DATE Herewith	GROUP Unknown

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